

# Notice of Allowability

Application No.

10/765,599

Examiner

Long Nguyen

Applicant(s)

BARNETT ET AL.

Art Unit

2816

## -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the amendment filed on 4/12/06 and the telephone interview on 6/7/06.
2. ☒ The allowed claim(s) is/are 1,2,5,6,11 and 12.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All b) ☐ Some\* c) ☐ None of the:
    1. ☐ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

### Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413), Paper No./Mail Date 6/7/06.
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

**EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Jim Brady on 6/7/06.

The application has been amended as follows:

**In The Claims**

Claim 1, line 17, "device is a" has been changed to --devices are--.

Claim 1, line 17, "device and" has been changed to --devices and--.

Claim 1, line 18, "device is a" has been changed to --devices are--.

Claim 1, line 18, "device." has been changed to --devices.--.

Claim 5, lines 7-9, "a plurality of circuit elements having asymmetric signal handling characteristics coupled in parallel and establishing a plurality of parallel asymmetric signal paths; and" has been changed to --first and second circuit elements coupled in parallel, said first circuit elements having symmetric signal handling characteristics, said second circuit elements having symmetric signal handling characteristics, said first and second circuit elements having asymmetric signal handling characteristics, said first and second circuit elements in each respective signal treating circuit segment of said first and second signal treating circuit segments coupled in parallel for establishing a plurality of parallel asymmetric signal paths; and--.

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Claim 5, line 11, "selected" has been changed to --said first and second--.

Claim 5, line 12, "of said plurality of circuit elements" has been deleted.

Claim 5, lines 13-16, "effecting substantially balanced signal handling among particular circuit elements of said selected circuit elements having symmetric signal handling characteristics in each said respective signal treating circuit segment," has been changed to --effect operating said first circuit elements in substantial symmetry and said second circuit elements in substantial symmetry,--.

Claim 5, line 17, "said two" has been changed to --said first--.

Claim 5, line 17, "are a" has been changed to --are--.

Claim 5, line 17, "transistor and a" has been changed to --transistors and said second circuit elements are--.

Claim 5, line 18, "transistor." has been changed to --transistors.--.

Claim 11, line 7, "including two" has been changed to --including first and second--.

Claim 11, lines 8-9, "said two semiconductor circuit elements having different signal handling characteristics; and" has been changed to --said first semiconductor circuit elements having like signal handling characteristics, said second semiconductor circuit elements having like signal handling characteristics, said first and second semiconductor circuit elements of each signal handling section of said two signal handling sections having different signal handling characteristics; and--.

Claim 11, line 12, "appropriate" has been deleted.

Claim 11, lines 12-14, "respective semiconductor circuit elements of said two semiconductor circuit elements having like signal handling characteristics in substantial

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symmetry,” has been changed to --said first semiconductor circuit elements in substantial symmetry and said second semiconductor circuit elements in substantial symmetry,--.

Claim 11, line 15, “one of said two” has been changed to --said first--.

Claim 11, line 15, “is a” has been changed to --are--.

Claim 11, line 16, “transistor and one of said two” has been changed to --transistors and said second--.

Claim 11, line 16, “is a bipolar transistor.” has been changed to --are bipolar transistors--.

#### **Reasons For The Above Changes**

The above changes have been made so that the claims are clear.

#### **REASONS FOR ALLOWANCE**

2. Claims 1, 2, 5, 6, 11, and 12 are allowed.

Claims 1, 5 and 11 are allowed because the prior art of record fails to disclose or suggest the limitations that the first circuit/transistor devices/elements are metal-oxide transistors and the second circuit/transistor devices/elements are bipolar transistors in combination with the rest of the limitations recited in claims 1, 5 and 11.

Claims 2, 6 and 12 are allowed because they depend on claims 1, 5 and 11, respectively.

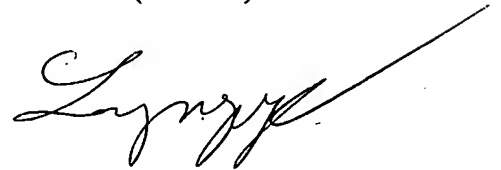
#### ***Conclusion***

3. Any inquiry concerning this communication or earlier communications from the examiner should be directly to Examiner Long Nguyen whose telephone number is (571) 272-1753. The Examiner can normally be reached on Monday to Thursday from 8:00am to 6:30pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tim Callahan, can be reached at (571) 272-1740. The fax number for this group is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



**LONG NGUYEN**  
**PRIMARY EXAMINER**